AHV FR 系列(機架式)



實驗室等級機架式

功率: 250W 特性及說明

The standard input voltage is 115 VAC, 50 - 100 Hz. Other voltages are available upon request. All units offer SLO-start feature with ramp up time less than 3 seconds.

The high voltage output is through a coax cable assembly located in the rear of the chassis. This chassis is not grounded and allows the output to be floated above the case by 50 VDC. All units feature arc protection and stability greater than 200 ppm/C°.

The FR Series high voltage power supplies are regulated by virtue of a resonant -sine wave Pulse Width Modulation topology. Here, a pulse driver converts a duty-cycle modulated square wave into a sinusoidal waveform by means of a high-Q resonant tank circuit. By using a resonant circuit, the high efficiency pulse dive is now converted to an amplitude modulated waveform which is stepped up by a high frequency high voltage transformer. The output

from the transformer is fed into a high voltage multiplier circuit which converts the AC into a high voltage DC output. The DC output is regulated by a high gain closed loop circuit which adjusts for both line and load variations to within 0.01%.

Since the IP series is both voltage and current regulated, two independent control loops operate in an automatic cross-over method. The front panel knobs for voltage and current set the limiting values.

The FR series may be both voltage and resistance programmed. An internal reference voltage of 11.5 VDC is adjusted down as shown in Figure 1 below and used as signal input to the non-inverting pin of an operational amplifier.

The closed feedback loop senses the output voltage and feeds this signal back to the inverting pin of the operational amplifier. Both voltage and current are regulated in this manner.

By adjusting the control with either a resistance to ground or an external voltage source, the feedback loop is adjusted which varies the regulated output. A voltage of 0 to 10 VDC placed on the voltage control line will vary the output voltage from 0 to 100%. Likewise, a voltage of 0 to 10 VDC placed on the current control line will wary the maximum output current from 0 to 100%.

Resistance programming is achieved by placing a resistance between the control line and ground. A value of 1 kOhm will lower the output (voltage or current) to 50%. In this configuration, it is possible to short the control line to zero to lower the output to the minimum. If an control voltage of above 12 volts is placed on the control line from an external source, the voltage will Zener clamp.

The interlock / enable must be connected to ground prior to turning on the unit.

If this line is not grounded, the output will remain at zero. In addition, since this unit is both voltage and current regulated, the front panel current control must set at a value higher than zero to achieve an output voltage.

Model	Output Voltage	Output Current	Ripple
FR-5P	0 to 5000V	0 to 50mA	<0.05%
FR-10P	0 to 10000V	0 to 25mA	<0.05%
FR-30P	0 to 30000V	0 to 8.33mA	<0.05%
FR-50P	0 to 50000V	0 to 5mA	<0.05%
FR-5N	0 to - 5000V	0 to 50mA	<0.05%
FR-10N	0 to - 10000V	0 to 25mA	<0.05%
FR-30N	0 to 30000V	0 to 8.33mA	<0.05%
FR-50N	0 to 50000V	0 to 5mA	<0.05%

此型可特殊訂製,歡迎來電詢問!

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